

AMENDMENTS TO THE ABSTRACT:

Please delete the Abstract in its entirety and replace it with the Substitute
Abstract shown on the page attached to this Amendment.



Substitute Abstract of the Disclosure

A system for providing a floating point sum includes an analyzer circuit configured to determine a first status of a first floating point operand and a second status of a second floating point operand based upon data within the first floating point operand and data with the second floating point operand respectively. In addition, the system includes a results circuit coupled to the analyzer circuit. The results circuit is configured to assert a resulting floating point operand containing the sum of the first floating point operand and the second floating point operand and a resulting status embedded within the resulting floating point operand.